TSUBAME 3.0

Tokyo, Japan. Aug. 1, 2017 - July 31, 2022

Overview

Tokyo Institute of Technology (Tokyo Tech) is operating TSUBAME2.5 supercomputer, which enjoys the peak performance of 5.76PFlops with 4224 NVIDIA Tesla K20X GPUs and exhibits world's top-class power-performance ratio. Its successor, TSUBAME3.0 will be deployed in 2017, which is designed not

only as a "post-peta" supercomputer with the peak performance around 20PFlops, but as a "big-data" supercomputer, with deeper storage and memory hierarchy. We are aggressively pursuing various R&D towards TSUBAME3.0 including system and software boosting big-data, deep learning, and AI applications.

Call for Proposal

Global Scientific Information and Computing center (GSIC) now calls for proposals for systems to be

TSUBAME 3.0. As a successor of prior TSUBAME series, the system should include, but not limited to:

* 12~24 PF performance in double precision

* 24~96 PF performance in half precision

- * 1.6~2.8 PB/s total memory bandwidth
- * 125~325 TiB total memory capacity
- * 55~185 Tbps total network injection bandwidth
- * 260~960 TB total local storage capacity
- * 8~15.5 PB high-speed storage system
- * 8~16 PB large-capacity storage system (option)
- * **ISV** application software
- * Virtual Desktop Infrastructure (option)
- * 1.00~1.30 PUE

Submission deadline: **Dec. 15, 2016 (extended!)**

Notification of acceptance: Jan. 30, 2017

July 31, 2017 **Camera-ready dealine:**

http://www.gsic.titech.ac.jp/sc16