



The diagram illustrates a system architecture with the following components and connections:

- PCH (Platform Controller Hub):** Connected to CPU 0 via DMI.
- CPU 0:** Connected to four DIMM modules and CPU 1 via QPI. It has x16 PCIe connections to the top PLX and the top OPA HFI.
- CPU 1:** Connected to four DIMM modules and CPU 0 via QPI. It has x16 PCIe connections to the bottom PLX and the bottom OPA HFI, and a x4 PCIe connection to the SSD.
- SSD (Solid State Drive):** Connected to CPU 1 via x4 PCIe.
- PLX (PCIe Switches):** Two switches, top and bottom, each connected to CPU 0 and CPU 1 via x16 PCIe. They also connect to OPA HFIs via x16 PCIe.
- OPA HFI (Optical Port Adaptor Host Fabric Interface):** Four modules, two connected to each PLX switch via x16 PCIe. They are connected to Edge switch #1 and Edge switch #2 via orange double-headed arrows.
- GPU 0, GPU 1, GPU 2:** Three GPU modules connected via NVLink (blue double-headed arrows). GPU 0 is connected to the top PLX and the top OPA HFI. GPU 1 is connected to the top PLX and the bottom OPA HFI. GPU 2 is connected to the bottom PLX and the bottom OPA HFI.
- Edge switch #1 and Edge switch #2:** Represented by orange double-headed arrows connected to the OPA HFIs.

